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INFORMATION DISCLOSURE STATEMENT

Applicants: Kaori YASUFUKU et al.

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OTHER DOCUMENT

(Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner Initial	
<i>JMK</i>	Inventors: Kaori Yasufuku et al., "Cap and Low Insertion Force Connector for Printed Circuit Board", Filed: August 23, 2000, Specification & Drawings of US Patent Application No. 09/643,963
<i>JMK</i>	Inventors: Kaori Yasufuku et al., "Connector for Module", Issued: August 21, 2001, US Patent Serial No. 6,278,610

Office of Patent
Information Disclosure
11-130
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Examiner *Felix D. Figueroa*Date Considered *12/18/01*

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.